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APPLICANTS

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This application is a CON of 09/906,060 07/17/2001 PAT 6,711,075

** FOREIGN APPLICATIONS *YES 12/15/04 TAT*

JAPAN 2000-218234 07/19/2000

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

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Foreign Priority claimed 35 USC 119 (a-d) conditions met	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	STATE OR COUNTRY JAPAN	SHEETS DRAWING 20	TOTAL CLAIMS 19	INDEPENDENT CLAIMS 4
Verified and Acknowledged <i>YES</i> <i>John J. Antonelli</i> Examiner's Signature	Initials <i>TAT</i>				

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TITLE

Semiconductor wafer, semiconductor chip, and manufacturing method of semiconductor device

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